THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants

Martin Gröpl

Serial No .:

10/652,256

Filed:

August 29, 2003

Title:

INTEGRATED CIRCUIT WITH OVERVOLTAGE PROTECTION

Docket:

36088

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. 1.97 and 1.98, Applicant is submitting herewith Form PTO-1449 listing references for consideration by the Examiner. Also submitted herewith is a legible copy of each non-U.S. reference. The undersigned hereby certifies that each item of information contained in this Supplemental Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Supplemental Information Disclosure Statement.

Respectfully submitted,

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Date: 2-26-04

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date indicated below.

John P. Murtaugh Name of Attorney for Applicant(s)

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